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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

LIST OF ARTICLES BY APPLICANT
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ATTY. DOCKET NO.
MI22-1171SERIAL NO.
09/292,132APPLICANT
Salman Akram et al.FILING DATE
April 14, 1999GROUP
2812

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

<i>Selby</i>	AR	Wolf, Ph.D., Stanley, "Silicon Processing for the VLSI Era - Volume 2: Process Integration," ©1990 Lattice Press, pages 212-213.					
	AS						
	AT						

EXAMINER

Samir

DATE CONSIDERED

9/20/04

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. K122-171	SERIAL NO. 09/21,112	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Salman Abram et al.		
					FILING DATE April 14, 1999	GROUP 1B/2	
U.S. PATENT DOCUMENTS							
*Examiner - Initials		Document Number	Date	Notes	Class	Subclass	Filing Date If Applicable
SJM	AA	5,554,871	09/10/96	Yamashita et al.			
SJM	AB	5,726,479	03/10/98	Morimoto et al.			
	AC						
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	AJ						
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		Document Number	Date	Country	Class	Subclass	Translation
SJM	AK	JP 9252117	07/22/97	Japan			Yes No
	AL	JP 9074189	03/18/97	Japan			Abs.
	AM	JP 8213605	05/20/96	Japan			Abs.
	AN	JP 81011309	07/12/96	Japan			Abs.
	AO	JP 8139215	05/21/96	Japan			Abs.
	AP	JP 8021108	02/20/96	Japan			Abs.
	AQ	JP 5251463	09/18/93	Japan			Abs.
SJM	AR	JP 6063974	01/27/92	Japan			Abs.
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AS						
	AT						
	AU						
EXAMINER <i>S. Müller</i>			DATE CONSIDERED <i>9/21/04</i>				
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